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paperback. Book Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment. Pages Number: 424 Publisher: Beijing Nikko Pub. Date :2011-5-1. The book Freescale Semiconductor (formerly Motorola Semiconductor) 16-bit microcontroller family in mc9s12xs128 s12x modeled embedded system software described and hardware design. The book is 11 chapters. including Chapter 1 describes an embedded system of knowledge. learning errors and learning suggestions. Chapter 2 gives xs128 minimum system hardware. and a brief description s12xcpu (cpu12x). Chapter 3 gives the first samples and codewai or engineering organizations. to complete the first s12x project started. Chapter 4 shows the components of the hardware-based embedded system development methods. Chapter 5 describes the serial communication interface sci. and gives the first instance with interrupt 1 ~ 5 describes the elements of learning a complete new mcu (knowledge points) of entry 6 to 12 chapters introduce gpio applications (keyboard. led and lcd). timer (including pwm). serial peripheral interface spi. flash memory. online programming. can bus. a / d conversion and s12xs128 other modules. etc. The appendix gives relevant information in this book source code examples. supporting data. relevant information and commonly used chip software tools can be downloaded at the Northern Centre or Suzhou University Press. Freescale Embedded Systems R & D Center Web site to download this book for college students on professional and graduate students for high school textbooks or reference books are also available for embedded systems development and research staff as a reference and training materials. Contents: Chapter 1 Overview 1.1 embedded system definition. origin and characteristics of embedded systems 1.1.1 Definition 1.1.2 The origin and embedded systems and microcontrollers relationship 1.1.3 Features 1.2 embedded systems embedded systems of knowledge. learning and learning errors suggest knowledge of embedded systems 1.2.1 1.2.2 1.2.3 Misunderstanding...



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